

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING SENT VIA Facsimile TO: COMMISSIONER OF PATENTS AND TRADEMARKS, Box 1450, Alexandria VA. 22313, on September 27, 2004 by: Teri McDonald

 9/27/04

Signature & Date

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: David J. Hathaway, et al.	Date: September 27, 2004
Serial Number: 10/078,876	Examiner: Joseph D. Torres
Filed: 2/20/02	Group Art Unit: 2133
Title: Method for Reducing Switching Activity During a Scan Operation with Limited Impact on the Test Coverage of an Integrated Circuit	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

AMENDMENT

Sir:

Responsive to the Office Action dated July 27, 2004, the following Amendment is being submitted to place the above-identified application in prima facie condition for allowance.

Kindly amend the above-identified application as follows:

10/078,876

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